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THE UNITED STATES PATENT AND TRADEMARK OFFICE

In re Applicant:

Rolls, et al.

§ Atty. Docket No.: 5181-41201

§  
§ Group Art Unit: 2836

Patent No.: 6,750,562 *b2*

Issued: June 15, 2004

§ I hereby certify that this correspondence is being deposited with the  
§ U.S. Postal Service as First Class Mail in an envelope addressed to:  
§ Commissioner for Patents, P.O. Box 1450, Alexandria, VA 22313-  
§ 1450, on the date indicated below:

B. Noel Kivlin

Signature of Representative

*8-31-04*

Date

*83*

Signature

→ Serial No.: 10/032,808

Filed: July 13, 2001

For: FAN CONTROL MODULE FOR  
A SYSTEM UNIT

TRANSMITTAL LETTER

**Certificate**

Commissioner for Patents  
P.O. Box 1450  
Alexandria, VA 22313-1450

SEP 09 2004

of Correction

Dear Sir:

Transmitted herewith for filing in the captioned case are the following:

- (1) Form PTO-1050, **submitted in duplicate**. Errors which occur at important points in the captioned patent or which may otherwise affect the understanding or interpretation of the patent are thereon corrected.
- (2) A return postcard to acknowledge receipt of these materials. Please stamp and return this postcard to the undersigned.

All of the errors shown in PTO-1050 are due to Patent Office oversights. A Certificate of Correction is requested under 35 U.S.C. §254. Applicants believe that no fees are required, however, should any fees be required, please deduct them from Meyertons, Hood, Kivlin, Kowert & Goetzel, P.C. Deposit Account No. 501505\5181-41201/BNK.

Respectfully submitted,



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Date: 8-31-04

## UNITED STATES PATENT AND TRADEMARK OFFICE

**CERTIFICATE OF CORRECTION**PATENT NO. : 6,750,562 *B2*

DATED : June 15, 2004

INVENTOR(S) : Rolls, et al.

It is certified that error appears in the above-identified patent and that said Letters Patent is hereby corrected as shown below:

**Column 14,**

Line 65, please insert the following:

- 22. The method of claim 21, further comprising the fan control module receiving power from a power supply, and the fan control module providing electrical noise isolation to isolate system components from electrical noise generated by the fans.
- 23. The fan control module of claim 21, wherein the fan control module individually controls the speed of two pairs of fans with a respective control loop being provided for each pair of fans.
- 24. The method of claim 21, wherein the fan control module individually controls the speed of a first fan for drawing air into the system unit and a second fan for driving air over a processor module in the system unit.
- 25. The method of claim 23, wherein a first fan of each pair of fans is a system fan for drawing air into the system unit and a second fan of each pair of fans is a processor fan for driving air over a processor module in the system unit. --

**MAILING ADDRESS OF SENDER:**

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No. of add'l. copies

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- 24. The method of claim 21, wherein the fan control module individually controls the speed of a first fan for drawing air into the system unit and a second fan for driving air over a processor module in the system unit.
- 25. The method of claim 23, wherein a first fan of each pair of fans is a system fan for drawing air into the system unit and a second fan of each pair of fans is a processor fan for driving air over a processor module in the system unit. –

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